









### 4.14 Real World Architectures

- In 1985, Intel introduced the 32-bit 80386.
- It also had no built-in floating-point unit.
- The 80486, introduced in 1989, was an 80386 that h ad built-in floating-point processing and cache mem ory.

CSC3501 - S.J. Park

- The 80386 and 80486 offered downward compatibili ty with the 8086 and 8088.
- Software written for the smaller word systems was di rected to use the lower 16 bits of the 32-bit registers

#### 4.14 Real World Architectures

 Currently, Intel's most advanced 32-bit microprocess or is the Pentium 4.

CSC3501 - S.J. Park

CSC3501 - S.J. Park

- It can run as fast as 3.8 GHz. This clock rate is nearl y 800 times faster than the 4.77 MHz of the 8086.
- Speed enhancing features include multilevel cache and instruction pipelining.
- Intel, along with many others, is marrying many of th e ideas of RISC architectures with microprocessors t hat are largely CISC.

# 4.14 Real World Architectures

- The MIPS family of CPUs has been one of the most successful in its class.
- In 1986 the first MIPS CPU was announced.
- It had a 32-bit word size and could address 4GB of memory.
- Over the years, MIPS processors have been used in general purpose computers as well as in games.
- The MIPS architecture now offers 32- and 64-bit ver sions.
- 9

## 4.14 Real World Architectures

- MIPS was one of the first RISC microprocessors.
- The original MIPS architecture had only 55 different i nstructions, as compared with the 8086 which had o ver 100.
- MIPS was designed with performance in mind: It is a load/store architecture, meaning that only the load a nd store instructions can access memory.
- The large number of registers in the MIPS architectu re keeps bus traffic to a minimum.

How does this design affect performance?

Assembly Language

CSC3501 - S.J. Park

- To command a computer, you must understand its la nguage.
  - Instructions: words in a computer's language
  - Instruction set: the vocabulary of a computer's language
- Instructions indicate the operation to perform and the operands to use.
  - Assembly language: human-readable format of instructions
- Machine language: computer-readable format (1's and 0's) We introduce the NUPC each it at the second sec
- We introduce the MIPS architecture.
  Developed by John Hennessy and his colleagues at Stanfor
  - d and in the 1980's.
    Used in many commercial systems, including Silicon Graphi
  - cs, Nintendo, and Cisco
- Once you've learned one architecture, it's easy to le arn others.

Copyright © 2007 Elsevier

## John Hennessy

10

- President of Stanford University
- Professor of Electrical Engineeri ng and Computer Science at Sta nford since 1977
- Coinvented the Reduced Instruction Set Computer (RISC)
- Developed the MIPS architectur e at Stanford in 1984 and cofoun ded MIPS Computer Systems
- As of 2004, over 300 million MIP S microprocessors have been s

old

Copyright © 2007 Elsevie



CSC3501 - S.J. Park

6-<12>



Copyright © 2007 Elsevier

6-<13>













Copyright © 2007 Elsevie









